

## INVENTOR INFORMATION

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Inventor Two Given Name:: Mark  
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## CORRESPONDENCE INFORMATION

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## APPLICATION INFORMATION

Title Line One:: CONFIGURATION AND/OR RECONFIGURATION OF  
Title Line Two:: INTEGRATED CIRCUIT DEVICES THAT INCLUD P  
Title Line Three:: ROGRAMMABLE LOGIC AND MICROPROCESSOR CIR  
Title Line Four:: CUITRY  
Total Drawing Sheets:: 5  
Formal Drawings?:: Yes  
Application Type:: Utility  
Docket Number:: ALT/195CON  
Secrecy Order in Parent Appl.?:: No

## REPRESENTATIVE INFORMATION

Representative Customer Number:: 36981  
Registration Number One:: 39774  
Registration Number Two:: 45241  
Registration Number Three:: 26183  
Registration Number Four:: 31069  
Registration Number Five:: 40210

CONTINUITY INFORMATION

This application is a:: CONTINUATION OF  
> Application One:: 09/879303  
Filing Date:: 06-12-2001

Which is a::NON PROV. OF PROVISIONAL  
>> Application Two:: 60/211094  
Filing Date:: 06-12-2000

Source:: PrintEFS Version 1.0.1